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emulation sleep clocks "device under test" locks

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[... a method for checking lock step consistency between an in circuit **emulation** and a microcontroller ...](#)

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C Nemecek - US Patent 6,922,821, 2005 - freepatentsonline.com

... of the FPGA implementation of an **emulator for the ... or when system encounters a breakpoint (eg, **sleep**, stall, internal ... However, using a **clock**, which is two times ...**

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C Nemecek, S Roe - US Patent 7,089,175, 2006 - Google Patents

... to the base station, the interface having a **clock signal me ... microcontroller can be placed in a **sleep** mode so ... use with a combined In-Circuit **Emulation** system and ...**

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C Nemecek, S Roe - US Patent 7,236,921, 2007 - Google Patents

... If the microcontroller is not in a **sleep mode, the ... An In-Circuit **Emulation** system consistent with certain trailer perspective. having a microcontroller **clock**. ...**

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C Nemecek, S Roe - US Patent 7,162,410, 2007 - Google Patents

... The In-Circuit **Emulation system has a microcontroller operating in **lock-step** synchronization with ... operates with its own gatekeeper **clock** independent of the ...**

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